I claim:

1. A method comprising:

instructing by a base node of a coalesced system to at least one other node of the coalesced system to start a process related to private memory;

starting the process by each of the at least one other node on each of the at least one other node, the process accessing private memory on each of the at least one other node; and,

reporting back results of the process by each of the at least one other node to the base node.

2. The method of claim 1, further initially comprising receiving a request from a user to perform system-wide functionality related to private memory, the process related to private memory based on the system-wide functionality requested.

3. The method of claim 1, further initially comprising starting the process by the base node on the base node, the process accessing private memory of the base node.

4. The method of claim 1, further comprising prior to starting the process by each of the at least one other node, receiving instruction from the base node by each of the at least one other node to start the process related to private memory.

5. The method of claim 1, further comprising receiving the results of the process from each of the at least one other node by the base node.

6. The method of claim 5, further comprising reporting the results received back from each of the at least one other node by the base node to a user having previously requested system-wide functionality related to private memory be performed, the process related to private memory based on the system-wide functionality requested.

7. The method of claim 1, wherein instructing by the base node to the at least one other node to start the process related to private memory comprises one of multicasting a start-process message by the base node to the at least one other node and unicasting the start-process message by the base node to each of the at least one other node.

8. The method of claim 1, wherein instructing by the base node to the at least one other node to start the process related to private memory comprises creating a parent process by the base node at the base node and creating a child process by the parent process at each of the at least one other node.

9. The method of claim 1, wherein instructing by the base node to the at least one other node to start the process related to private memory comprises employing a multi-processor protocol (MPP).

10. A system comprising:

a plurality of nodes coalesced to function as the system and divided into a base node allowing direct user interaction therewith and other nodes allowing indirect user interaction therewith through the base node; and,

private memory at each of the plurality of nodes, the private memory of a node

accessible only by the node,

the base node governing performance by each of the plurality of nodes of a process related to private memory of each of the plurality of nodes to effectuate user-initiated, system-wide performance of functionality related to private memory, the process related to private memory based on the functionality initiated.

11. The system of claim 10, further comprising a network to which each of the plurality of nodes is connected such that the network assists coalescing of the plurality of nodes as the system.

12. The system of claim 10, further comprising at least one of a keyboard and a display connected to the base node to assist direct user interaction with the base node.

13. The system of claim 10, wherein at least one of the plurality of nodes each comprise a computer having a processor in addition to having the private memory.

14. The system of claim 10, wherein at least one of the plurality of nodes each comprise a non-computer device having a processor in addition to having the private memory.

15. The system of claim 10, wherein the private memory at each of the plurality of nodes comprises one of: flash memory, expansion read-only memory (ROM), and error log memory.

16. The system of claim 10, wherein the base node governs the performance by each of the plurality of nodes of the process to effectuate the user-initiated, system-wide performance of functionality related to private memory by employing a multi-processor protocol (MPP).

17. The system of claim 16, wherein the MPP is part of one of an operating system (OS) of the base node and boot-time services of the base node.

18. An article comprising:

a computer-readable medium; and,

means in the medium for governing performance by each of a plurality of nodes coalesced to function as a system of a process related to private memory of each of the plurality of nodes, to effectuate user-initiated, system-wide performance of functionality related to private memory, the process related to private memory based on the functionality initiated.

19. The article of claim 18, wherein the means is a recordable data storage medium.

20. The article of claim 18, wherein the means is a modulated carrier signal.